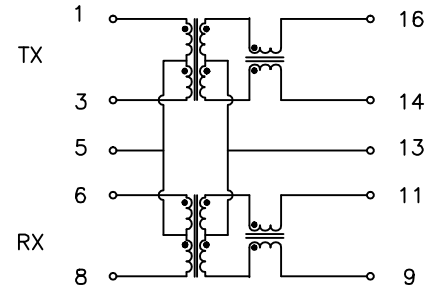


PCB SUGGEST PAD LAYOUT



ELECTRICAL SPECIFICATIONS :

1. TURNS RATIO : TX = 1CT : 1CT \pm 3%
RX = 1CT : 1CT \pm 3%
2. OCL (100kHz/0.1V) = 350uH MIN.
3. HI-POT : 1500Vrms
5. INSERTION LOSS : 0.1-100MHz = -1.0 dB MAX.
6. RETURN LOSS : 30MHz = -18 dB MIN.
60MHz = -16 dB MIN.
80MHz = -12 dB MIN.
7. CMRR : 30MHz = -45 dB MIN.
50MHz = -40 dB MIN.
100MHz = -35 dB MIN.
8. RoHS COMPLIANT

UNLESS OTHERWISE SPECIFIED ALL TOLERANCES :
xx.x0 \pm 0.25
0.xx \pm 0.05

							TITLE 10/100 BASE-TX PCMCIA TRANSFORMER	
							DWG. NO. ATPL-367R	
RELEASE		05/27/2010	SHIRLEY	BETTY	KING	UNITS: M/M	SAFETY	
NO:	DESCRIPTION	DATE	BY	CHK	APPD	DATE	P/N:	SHEET 1 of 1
REVISIONS						05/27/10	DRAW SHIRLEY	